

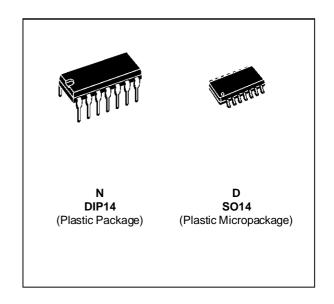
LM124,A - LM224,A LM324,A

LOW POWER QUAD OPERATIONAL AMPLIFIERS

■ LARGE VOLTAGE GAIN: 100dB

■ VERY LOW SUPPLY CURRENT/AMPLI : 375µA

 LOW INPUT BIAS CURRENT: 20nA
 LOW INPUT OFFSET VOLTAGE: 2mV
 LOW INPUT OFFSET CURRENT: 2nA
 WIDE POWER SUPPLY RANGE: SINGLE SUPPLY: +3V TO +30V DUAL SUPPLIES: ±1.5V TO ±15V



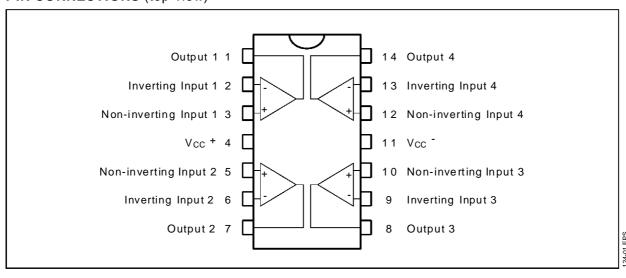
DESCRIPTION

These circuits consist of four independent, high gain, internally frequency compensated operational amplifiers which were designed specifically for automotive and industrial control systems. They operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

ORDER CODES

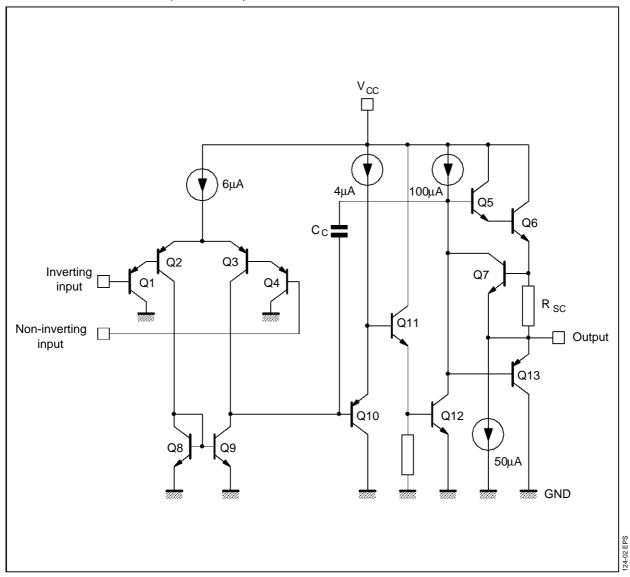
Part	Temperature	Package		J. a.ca. 0			
Number	Range	N	D				
LM124,A	-55°C, +125°C	•	•				
LM224,A	-40°C, +105°C	•	•				
LM324,A	0°C, +70°C	•	•	24-01.TBL			
Example: LM224N							

PIN CONNECTIONS (top view)



November 1995 1/11

SCHEMATIC DIAGRAM (1/4 LM124)



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter		LM124,A	LM224,A	LM324,A	Unit		
V_{cc}	Supply Voltage			±16 or 32				
V_{i}	Input Voltage			-0.3 to +32				
V_{id}	Differential Input Voltage - (*)		+32	+32	+32	V		
P _{tot}		N Suffix D Suffix	500	500 400	500 400	mW mW		
-	Output Short-circuit Duration - (note 1)	Infinite					
I _{in}	Input Current – (note 6)		50	50	50	mA		
T _{oper}	Operating Free Air Temperature	Range	-55 to +125	-40 to +105	0 to +70	°C		
T _{stg}	Storage Temperature Range		-65 to +150	-65 to +150	-65 to +150	°C		

^{(*) -} Either or both input voltages must not exceed the magnitude of V_{CC}^+ or V_{CC}^- .



ELECTRICAL CHARACTERISTICS

 V_{CC}^+ = +5V, V_{CC}^- = Ground, V_O = 1.4V, T_{amb} = +25°C (unless otherwise specified)

Symbol	Parameter		LM124A - LM224A LM324A			LM124 - LM224 LM324		
_			Тур.	Max.	Min.	Тур.	Max.	
V _{io}	Input Offset Voltage (note 3) $T_{amb} = +25^{\circ}C$ $LM324$ $T_{min.} \leq T_{amb} \leq T_{max}.$ $LM324$		2	3 5		2	5 7 7 9	mV
l _{io}	Input Offset Current $T_{amb} = +25^{\circ}C$ $T_{min.} \le T_{amb} \le T_{max}.$		2	10 30		2	30 100	nA
l _{ib}	Input Bias Current (note 2) $T_{amb} = +25^{\circ}C$ $T_{min.} \le T_{amb} \le T_{max}.$		20	50 100		20	150 300	nA
A _{vd}	Large Signal Voltage Gain $(V_{CC}^+ = +15V, R_L = 2k\Omega, V_O = 1.4V \text{ to } 11.4V)$ $T_{amb} = +25^{\circ}C$ $T_{min.} \le T_{amb} \le T_{max}$.	50 25	100		50 25	100		V/mV
SVR	Supply Voltage Rejection Ratio $(R_S \le 10k\Omega)$ $(V_{CC}^+ = 5V \text{ to } 30V)$ $T_{amb} = +25^{\circ}C$ $T_{min.} \le T_{amb} \le T_{max}$.	65 65	110		65 65	110		dB
Icc	$ \begin{array}{ll} \text{Supply Current, all Amp, no load} \\ T_{amb} = +25^{\circ}\text{C} & \text{V_{CC} = +5V} \\ & \text{V_{CC} = +30V} \\ T_{min.} \leq T_{amb} \leq T_{max}. & \text{V_{CC} = +5V} \\ & \text{V_{CC} = +30V} \\ \end{array} $		0.7 1.5 0.8 1.5	1.2 3 1.2 3		0.7 1.5 0.8 1.5	1.2 3 1.2 3	mA
Vicm	Input Common Mode Voltage Range $(V_{CC} = +30V)$ - (note 4) $T_{amb} = +25^{\circ}C$ $T_{min.} \le T_{amb} \le T_{max.}$	0		V _{CC} -1.5 V _{CC} -2	0 0		V _{CC} -1.5 V _{CC} -2	V
CMR	Common-mode Rejection Ratio (R _S \leq 10k Ω) T_{amb} = +25°C $T_{min.}$ \leq T_{amb} \leq T_{max}	70 60	80		70 60	80		dB
lo	Output Short-circuit Current ($V_{id} = +1V$) $V_{CC} = +15V$, $V_0 = +2V$	20	40	60	20	40	60	mA
I _{sink}	Output Sink Current (V_{id} = -1V) V_{CC} = +15V, V_{o} = +2V V_{CC} = +15V, V_{o} = +0.2V	10 12	20 50		10 12	20 50		mΑ μΑ

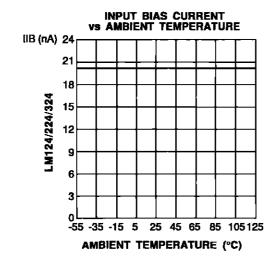
ELECTRICAL CHARACTERISTICS (continued)

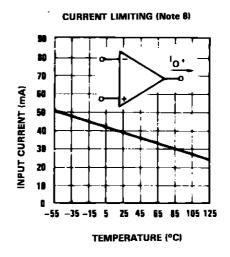
Symbol	Parameter		LM124A - LM224A LM324A			LM124 - LM224 LM324		
j		Min.	Тур.	Max.	Min.	Тур.	Max.	
V _{OH}	$\begin{array}{l} \text{High Level Output Voltage} \\ (\text{V}_{\text{CC}} = +30\text{V}) \\ \text{T}_{\text{amb}} = +25^{\text{O}}\text{C} \\ \text{T}_{\text{min.}} \leq \text{T}_{\text{amb}} \leq \text{T}_{\text{max.}} \\ \text{T}_{\text{amb}} = +25^{\text{O}}\text{C} \\ \text{T}_{\text{min.}} \leq \text{T}_{\text{amb}} \leq \text{T}_{\text{max.}} \\ (\text{V}_{\text{CC}} = +5\text{V}, \text{R}_{\text{L}} = 2\text{k}\Omega) \\ \text{T}_{\text{amb}} = +25^{\text{O}}\text{C} \\ \text{T}_{\text{min.}} \leq \text{T}_{\text{amb}} \leq \text{T}_{\text{max.}} \\ \end{array}$	26 26 27 27 3.5 3	27 28		26 26 27 27 3.5 3	27 28		V
V _{OL}	Low Level Output Voltage (R _L = 10kΩ) $T_{amb} = +25^{\circ}C$ $T_{min.} \le T_{amb} \le T_{max}$.		5	20 20		5	20 20	mV
SR	Slew Rate (V_{CC} = 15V, V_I = 0.5 to 3V, R_L = 2k Ω , C_L = 100pF, T_{amb} = +25 $^{\circ}$ C, unity gain)		0.4			0.4		V/µs
GBP	Gain Bandwidth Product ($V_{CC} = 30V$ $f = 100kHz$, $T_{amb} = +25^{\circ}C$, $V_{in} = 10mV$ $R_L = 2k\Omega$, $C_L = 100pF$)		1.3			1.3		MHz
THD	Total Harmonic Distortion (f = 1kHz, A_V = 20dB, R_L = 2k Ω , V_O = 2 V_{pp} C_L = 100pF, T_{amb} = +25 $^{\circ}$ C, V_{CC} = 30V)		0.015			0.015		%
e _n	Equivalent Input Noise Voltage (f = 1kHz, $R_s = 100\Omega$, $V_{CC} = 30V$)		40			40		$\frac{\text{nV}}{\sqrt{\text{Hz}}}$
DVio	Input Offset Voltage Drift		7	30		7	30	μV/°C
Dlio	Input Offset Current Drift		10	200		10	200	pA/°C
V ₀ 1/V ₀ 2	Channel Separation (note 5) $1kHz \le f \le 20kHz$		120			120		dB

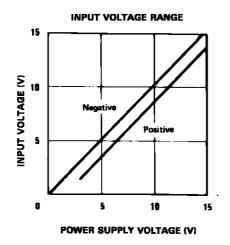
Notes: 1. Short-circuits from the output to V_{CC} can cause excessive heating if V_{CC} > 15V. The maximum output current is approximately 40mA independent of the magnitude of V_{CC}. Destructive dissipation can result from simultaneous short-circuit on all amplifiers.

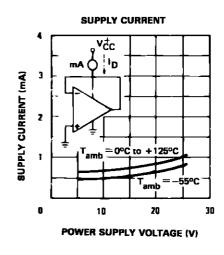
- 2. The direction of the input current is out of the IC. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines.
- 3. $V_0 = 1.4V$, $R_s = 0\Omega$, $5V < V_{CC}^+ < 30V$, $0 < V_{ic} < V_{CC}^+ 1.5V$
- 4. The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3V. The upper end of the common-mode voltage range is V_{CC}⁺ 1.5V, but either or both inputs can go to +32V without damage.
- 5. Due to the proximity of external components insure that coupling is not originating via stray capacitance between these external parts. This typically can be detected as this type of capacitance increases at higher frequences.
- 6. This input current only exists when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistor becoming forward biased and thereby acting as input diodes clamps. In addition to this diode action, there is also NPN parasitic action on the IC chip. this transistor action can cause the output voltages of the Op-amps to go to the Vcc voltage level (or to ground for a large overdrive) for the time duration than an input is driven negative.

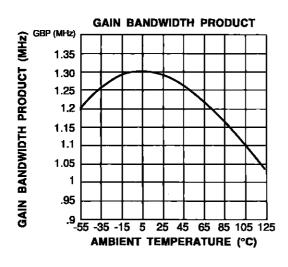
This is not destructive and normal output will set up again for input voltage higher than -0.3V.

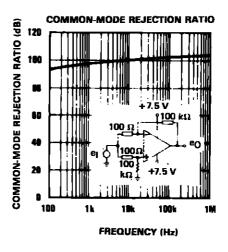












124-08.EPS

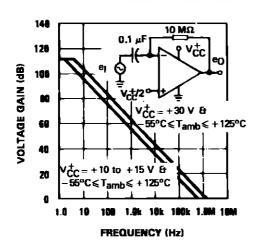
124-06.EPS

124-04.EPS

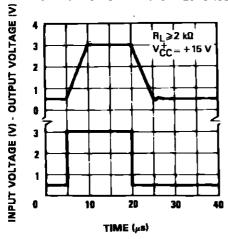
124-07.EPS

124-03.EPS

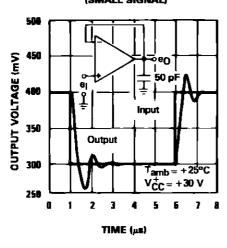
OPEN LOOP FREQUENCY RESPONSE



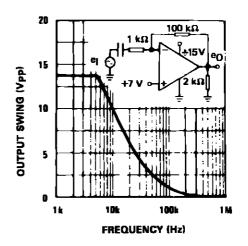
VOLTAGE FOLLOWER PULSE RESPONSE



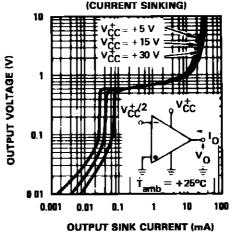
VOLTAGE FOLLOWER PULSE RESPONSE (SMALL SIGNAL)



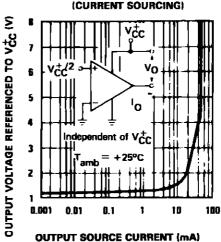
LARGE SIGNAL FREQUENCY RESPONSE

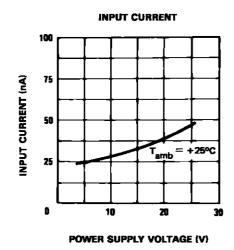


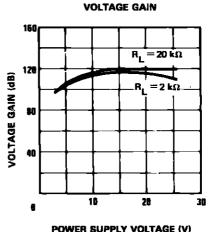
OUTPUT CHARACTERISTICS (CURRENT SINKING)



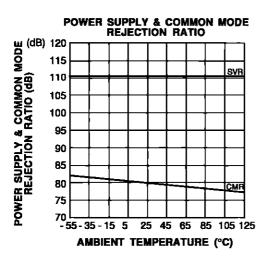
OUTPUT CHARACTERISTICS (CURRENT SOURCING)

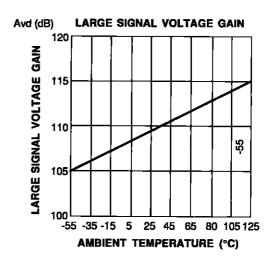






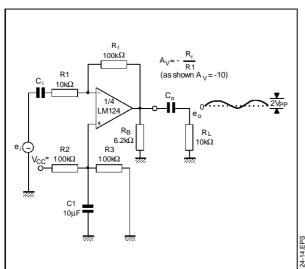
POWER SUPPLY VOLTAGE (V)



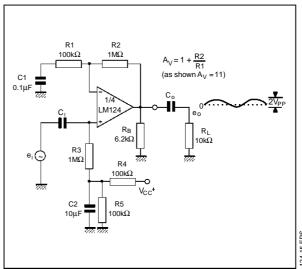


TYPICAL SINGLE - SUPPLY APPLICATIONS

AC COUPLED INVERTING AMPLIFIER



AC COUPLED NON-INVERTING AMPLIFIER

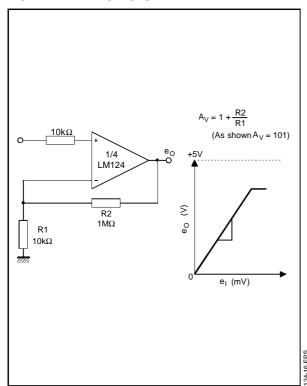


124-13.EPS

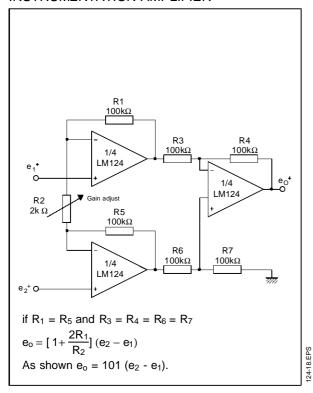
124-10.EPS

TYPICAL SINGLE - SUPPLY APPLICATIONS

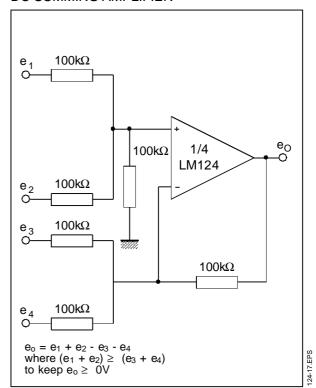
NON-INVERTING DC GAIN



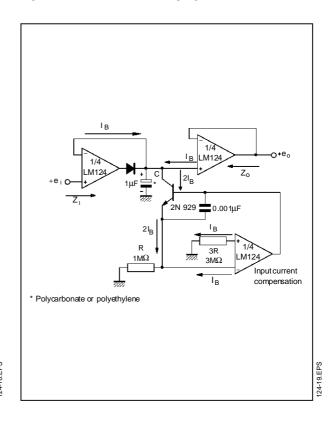
HIGH INPUT Z ADJUSTABLE GAIN DC INSTRUMENTATION AMPLIFIER



DC SUMMING AMPLIFIER



LOW DRIFT PEAK DETECTOR

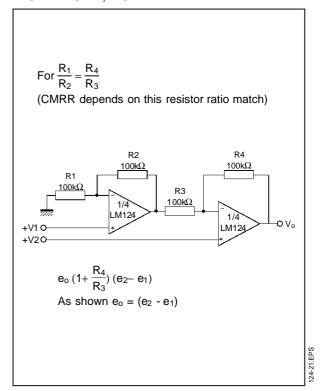


TYPICAL SINGLE - SUPPLY APPLICATIONS

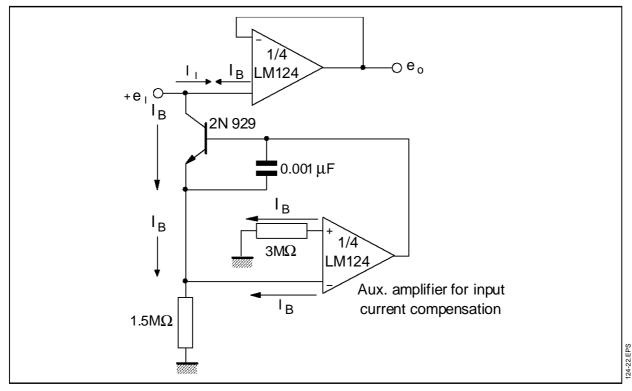
ACTIVER BANDPASS FILTER

$R_{1000\Omega}$ $R_{1000\Omega}$

HIGH INPUT Z, DC DIFFERENTIAL AMPLIFIER

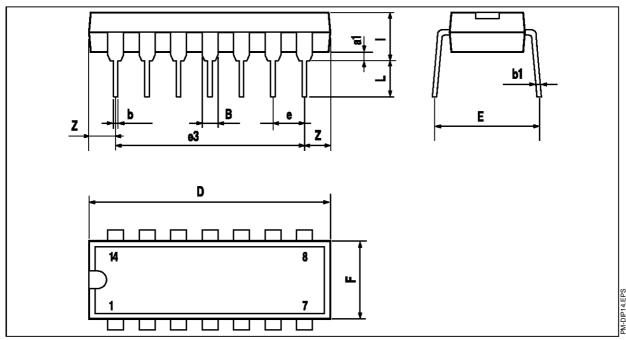


USING SYMMETRICAL AMPLIFIERS TO REDUCE INPUT CURRENT (GENERAL CONCEPT)



PACKAGE MECHANICAL DATA

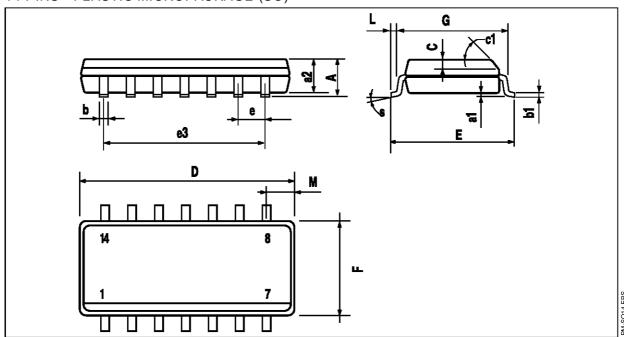
14 PINS - PLASTIC DIP OR CERDIP



Dimensions		Millimeters		Inches		
Dimensions	Min.	Тур.	Max.	Min.	Тур.	Max.
a1	0.51	1		0.020		
В	1.39		1.65	0.055		0.065
b		0.5			0.020	
b1		0.25			0.010	
D			20			0.787
Е		8.5			0.335	
е		2.54			0.100	
e3		15.24			0.600	
F			7.1			0.280
į			5.1			0.201
L		3.3			0.130	
Z	1.27		2.54	0.050		0.100

PACKAGE MECHANICAL DATA

14 PINS - PLASTIC MICROPACKAGE (SO)



Dimensions		Millimeters			Inches	
Dimensions	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			1.75			0.069
a1	0.1		0.2	0.004		0.008
a2			1.6			0.063
b	0.35		0.46	0.014		0.018
b1	0.19		0.25	0.007		0.010
С		0.5			0.020	
c1			45°	(typ.)		
D	8.55		8.75	0.336		0.334
E	5.8		6.2	0.228		0.244
е		1.27			0.050	
e3		7.62			0.300	
F	3.8		4.0	0.150		0.157
G	4.6		5.3	0.181		0.208
L	0.5		1.27	0.020		0.050
М			0.68			0.027
S			8° (1	max.)	•	•

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